TuningGenie — an autotuning framework for optimization of parallel applications

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Paper proposes software tool for automatic generation of autotuners — special kind of applications to optimize running time of parallel applications in target computing environment. Traditional approach to autotuners creation is amplified by utilizing facilities of rule-based rewriting framework for code transformation purposes. Experimental results show effectiveness of this approach and exhibit ease of use of presented framework.

Introduction

In software development for any field optimization phase is both significant and complicated. This phase is extremely sophisticated and resource-intensive when major aim is to create application which will be efficient in various multiprocessor environments. An autotuning [1], [2] is a modern approach for resolving this issue. Autotuner is separate software which optimizes target software. Optimization typically consists of generating different, but predefined in common sense, variations of optimized software and selecting the most efficient one based on empirical evaluation in target environment. A usually chief criterion of effectiveness is running time. Main advantage of autotuning is in one-time optimization for execution environment — derived variation is most effective until environment configuration remains changeless. Autotuning methodology on contrary to parallel compilers does not require complex source code analysis. It makes possible to create generalized framework which is independent from host programing language, application domain and can be applied for optimizing software for various computing environments - from mobile devices to hybrid clusters. Object of this article is to introduce such framework.

Autotuning — state of the art

There has been solid investigation of search-based autotuning for high-capacity computing. Among them it's worth to mention such well-known systems as ATLAS [3] or FFTW [4]. They utilize autotuning approach to generate high-performance platform-specific libraries in target operational environment. Weakness of this approach is that such solutions are not generic — they propose highly-efficient implementation of most common operations for specific application domain.

Contrary to previous systems Atune-IL [2] proposes an instrumentation language for autotuning which is usable with any programming language, application domain, supports nested parallelism and uses #pragma-based approach to define domain where optimal configuration is searched. In general it allows defining different values for inner program variables that will be probed during instrumentation. For code transformations StringTemplate [5] is used.

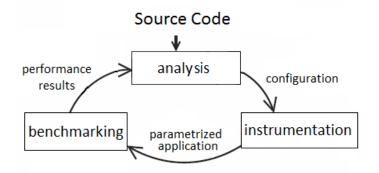
Problem statement

Motivation for this research is idea to create autotuning framework that is independent from programming language and application domain, is able to perform structural code transformations, provides performance monitoring support and allows easily introducing numerical information about target platform (RAM/CPU access speed, basic arithmetic operations execution time, etc.) to parallel programs.

TuningGenie

TuningGenie is autotuning framework that works with source code of application. It uses similar to described in [2] #pragma-based approach to pass expert knowledge from developer to tuner. It's important to mention that it doesn't automatically parallelizes input application but generates separate version of program for each defined by developer parameters configuration. To achieve desired flexibility in code conversion TuningGenie uses TermWare [6] — rule-based rewriting framework. TermWare transforms source code into term, executes rewriting rules and transforms resulted term back to text representation. Such technology allows not only perform simple substitutions in source code but to enter structural changes in program's algorithms which excels capabilities of existing autotuning frameworks. For instance, it can experiment with data traversal directions (described below pragma *bidirectionalCycle*). It also contains knowledge base to store facts and operate with them in term transformation phase (this feature is used in described below pragma *calculatedValue*). TermWare uses own syntax for rules definition, comes with "out of the box" parsers for Java and FORTRAN language and can be easily extended to add support for other programming languages. In scope of this work probing part of TuningGenie was implemented only for Java applications. It uses custom class loader to reload and benchmark optimized software variations.

In general following diagram describes tuning cycle:



Currently framework contains three main pragmas for tuning configuration definition:

- tuneAbleParam defines range of values for numeric variable. Can be used, for instance, to find size of optimal data decomposition in algorithms that fit "divide-and-conquer" or "geometric decomposition" patterns [7]. An example of resource-critical program which performance considerably depends on granularity of data decomposition is considered in previous work of author "Automatic optimization of meteorological forecasting problem" [8];
- *calculatedValue* specifies function that will be calculated during instrumentation and variable that will be initialized by this function's result. Allows execute benchmark in target environment and embed empirically-derived data into optimized program;
- *bidirectionalCycle* this pragma points that direction of cycle iteration does not affect result of calculation and can be changed to inverse. Allows TuningGenie to experiment with data traversal (efficiency of using system caches in particular) and see impact on application's performance;

These three pragmas allow performing optimization for quite extensive class of applications but as a simple demo-example let's consider how TuningGenie can be applied for simple sorting algorithm optimization.

Experiment

As a demo example let's see how parallel sorting algorithm can be tuned. Chosen hybrid algorithm is based on classic parallel implementation of merge-sort. Modification consists in switching to insertion sequential sorting method when size of array becomes less than threshold. Such modification is reasonable since insertion sort is known to be faster on small arrays. Autotuner's task consists of empirically finding a value of this threshold. The only modification needed to perform it is a single *tuneAbleParam* pragma:

TuningGenie will consequently generate variations of source code for threshold value from [10 - 500] range with step equal to 10, compile and benchmark it. The fastest variation for target environment will be stored to be used in future calculations.

For benchmarking array with 2 million integers was sorted. Configuration of environment:

• Intel® CoreTM i5-2410M Processor (3M Cache, up to 2.90 GHz)

• 4 GB DDR2 RAM

Optimal value of threshold was proven to be 120 and this configuration was 23% faster than variation with initial threshold value equal to 10.

Conclusion

Autotuning is a powerful methodology for software optimization which also significantly shortens development time. Taking advantage of rewriting rules technique for code transformation considerably strengthens methodology of automatic software optimization. Presented framework allows abstracting software development away from target environment details while guaranteeing optimality of software's runtime execution. TuningGenie easily provides to applications empirically derived data about running environment and simplifies benchmarking of software. Experiment demonstrated ease of use and effectiveness of introduced framework. In further work functionality for performance data analysis can be added to framework. Usually a lot of configurations are probed during application tuning so structured and visualized information about their impact on performance can be very useful for developers.

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